IPN P17615 (Intel Corporation) IN THE UNI

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of:

Anandaroop Bhattacharya et al.

Application No.: 10/723,533

Filed: November 26, 2003

FOR AN INTEGRATED CIRCUIT

FOR AN INTEGRATED CIRCUIT

Examiner: Boris Leo Chervinksy

Art Unit: 2835

Confirmation No. 8659

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on the date shown below:

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AMENDMENT AND RESPONSE

Responsive to the Office Action mailed November 18, 2005, Applicants request that the Examiner please amend the application as follows and consider the following remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.